## Abstract Machines, Optimal Reduction, and Streams

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## Implementation of lambda-calculus

 reduction- Optimal reduction was introduced in J.J. Levy's PhD Thesis in 1978 and defined (by means of sharing graphs) by J. Lamping in 1990.
- Geometry of Interaction was introduced by J.-Y. Girard in 1985.
- In 1992, M. Abadi, J. Gonthier and J.-J. Levy made a link between optimal reduction and Geometry of Interaction.
- Virtual Reduction (Danos-Regnier 1993) is a way to make Gol close to optimal implementation.
- Directed Virtual Reduction (Danos-Pedicini-Regnier 1997) is a modification of the VR to ease implementation.
- PELCR (Pedicini-Quaglia 2007) is a complete implementation which permits parallel execution on multi-core machines.


## Is a Graph Reduction Technique ?

- The machine state is represented by a pair:
- a dynamic graph
- a list of pending actions.
- Any machine transition is obtained by applying the action $\alpha$ to the current graph $G$, from which we get a pair

$$
\alpha \cdot G \rightarrow\left(\Delta_{\alpha}, G \cup\{\alpha\}\right) .
$$

$\Delta_{\alpha}=\left\{\alpha_{1}, \beta_{1}^{\prime}, \ldots, \alpha_{m}, \beta_{m}^{\prime}\right\}$ is a set of residual actions to be added to pending actions already in the state and $G \cup\{\alpha\}$ is the updated dynamic graph.

## Machine Transition

## More precisely:

Let $A$ be the set of pending actions, so that the couple $(A, G)$ denotes the state of the machine. The transition associated to the action $\alpha \in A$ is then

$$
(A, G) \xrightarrow{\tau_{\alpha}}\left(A \backslash\{\alpha\} \cup \Delta_{\alpha}, G \cup\{\alpha\}\right)
$$

that is, residual actions $\Delta_{\alpha}$ are added to the list of pending actions.

The basic computational step is borrowed from half-combustion strategy of DVR it includes a symbolic computation in the algebraic structure associated to the graph (the dynamic monoid) and it is a generalisation of the algebraic computations at the base of Geometry of Interaction.

## A computation

The memory of the machine is initialized with an empty graph, so that the execution of a terminating program on the abstract machine is represented by the finite sequence of transitions

$$
\left(A^{0}, \emptyset\right) \xrightarrow{\tau_{\alpha}}\left(A^{1}, G^{1}\right) \xrightarrow{\tau_{\alpha_{1}}} \cdots \xrightarrow{\tau_{\alpha_{n-1}}}\left(A^{n}, G^{n}\right) \xrightarrow{\tau_{\alpha \eta}}\left(\emptyset, G^{n+1}\right)
$$

where $\alpha_{i} \in A^{i}$ for all $i=0, \ldots, n$. Note that the initial action set $A^{0}$ is the interpretation of the program, and the final graph $G^{n+1}$ represents the result of the evaluation.

In general, the execution of the machine may not terminate and, consequently, be represented by a possibly infinite sequence of elementary steps.

## Non termination

When executing on parallel machines, the sequence of residuals produced by non terminating execution on one machine and directed to another one can be infinite.

To cope with this situation we adopt streams as data structures for the pending actions.

Description of the Sequential Abstract Machine, whose setting is reminiscent of SECD machines, employed to give the operational semantics of $\lambda$-calculus.

## A bridging model

We introduce a formal description for multicore "functional" computation as a step to quantitatively study the behaviour of the PELCR implementation.

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As a starting poit we assume (what we already know) that PELCR is sound as a "parallel" operational semantics, this means that we do not care on reordering of actions since the computation of the normal form by using Geometry of interaction rules (shared optimal reduction) is local and asynchroous.

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Definition (PELCR Actions)
Given a dynamic graph $G$, which is a graph
$G=(V, E \subset V \times V)$ with edges labeled on the Girard dynamic algebra $\wedge^{*}$, we define an action $\alpha$ on $G$ as $\langle\epsilon, e, w\rangle$ where $\epsilon \in\{+,-\}^{2}, e=\left(v_{t}, v_{s}\right) \in V^{2}$ is an edge in $G$ and $w \in \Lambda^{*}$.

## PELCR-VM

We describe the pelcr virtual machine (PVM) as an abstract machine working on its state $(C, D)$.

- C contains the computational task: a stream of closures (FIFO).
- A closure is a signed edge.
- A signed edge $e$ is represented by a triplet $\left\langle\left(\varepsilon_{t}, \varepsilon_{s}\right),\left(v_{t}, v_{s}\right), w\right\rangle$, where: $\varepsilon_{t}, \varepsilon_{s} \in\{+,-\}$ are the target and source polarities of $e ; v_{t}, v_{s} \in V$ are the target and source nodes of $e$ and can be considered as their memory addresses; and $w \in M$ is the label of $e$.
- D represents the current memory, and contains environment elements.
- any environment element has a memory address $e_{i}$ and is called node.
- memory at address $e_{i}$ contains signed edges.


## Machine Representation

pending actions



Girard dynamic algebra is traditionally used in the execution formula of Geometry of Interaction, which is a power series of matrices. Consequently $\wedge^{*}$ is considered together with a formal sum of its elements, and consequently it is a (monoid) algebra.

## Girard dynamic algebra $\wedge^{*}$

The so-called Girard dynamic algebra $\wedge^{*}$ is the dynamic monoid generated by the constants $p, q$, and a family $W=\left\{w_{i}\right\}_{i}$ of exponential generators, with a morphism !(.), such that for any $u \in \Lambda^{*}$ :

$$
\begin{array}{lrl}
\text { (annihilation) } \quad x^{*} y & =\delta_{x y} \quad \text { for } x, y=p, q, w_{i}, \\
\text { (swapping) } \quad!(u) w_{i} & =w_{i}!!_{i}(u),
\end{array}
$$

where $\delta_{x y}$ is the Kronecker operator, $e_{i}$ is an integer associated with $w_{i}$ called the lift of $w_{i}, i$ is called the name of $w_{i}$ and we often write $w_{i, e_{i}}$ to explicitly note the lift of the generator. Notice that swapping and annihilation rules imply that for every $a, b \in \Lambda^{*}$ either $b^{*} a=0$ or it has a stable form, that is $\Lambda^{*}$ satisfies SF condition.

## Stable Form

For instance, setting $a=w_{1,2}$ and $b=!^{2} q$, by applying the annihilation rule we get:
$b^{*} a=\left(!^{2} q\right)^{*} w_{1,2}=!\left(!q^{*}\right) w_{1,2}=w_{1,2}!^{2}\left(!q^{*}\right)=w_{1,2}\left(!^{3} q\right)^{*}=a^{\prime} b^{\prime *}$
with $a^{\prime}=a$ and $b^{\prime}=!b$.

## Example: $(\Delta) /$



## Gol interpretation

The matrix with entries in the Girard dynamic algebra:

|  | $\left[\mathrm{ax}_{1}\right.$ | $[\mathrm{ax}]_{2}$ | $\left[\mathrm{ax}_{3}\right.$ | $\left[\mathrm{ax}_{3}\right.$ | [cut] ${ }_{1}$ | $[t]_{1}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $[\mathrm{ax}]_{1}$ | 0 | 0 | 0 | 0 | $q x_{2}+q x_{1} q$ | 0 |
| [ax] ${ }_{2}$ | 0 | 0 | 0 | 0 | $q x_{1} p+p$ | 0 |
| $[\mathrm{ax}]_{3}$ | 0 | 0 | 0 | 0 | $q!q+q!p$ | 0 |
| $[\mathrm{ax}]_{4}$ | 0 | 0 | 0 | 0 | $p$ | 1 |
| [cut] ${ }_{1}$ | $x_{2}^{*} q^{*}+q^{*} x_{1}^{*} q^{*}$ | $p^{*} x_{1}^{*} q^{*}+p^{*}$ | $+\left(!p^{*}\right) q^{*}$ | $p^{*}$ | p | 0 |
|  | 0 | 0 | 0 |  |  | 0 |

## The corresponding graph

Nodes are axioms, cuts and conclusions (terminal nodes):

$$
V=\left\{[a x]_{1},[a x]_{2},[a x]_{3},[a x]_{4},[c u t]_{1},[t]_{1}\right\}
$$

and edges $\left(\left(v_{t}, v_{s}\right), w\right)$ get a weight $w \in \Lambda^{*}$ where $v_{t}$ is the target node and $v_{s}$ is the source node. In this example, the "sparse" representation, consisting of the list of edges with a non-null weight, is more compact:

$$
\begin{aligned}
E=\{ & \left(\left([\text { cut }]_{1},[\mathrm{ax}]_{1}\right), q x_{1} q\right),\left(\left([\mathrm{cut}]_{1},[\mathrm{ax}]_{1}\right), q x_{2}\right),\left(\left([\mathrm{cut}]_{1},[\mathrm{ax}]_{2}\right), q x_{1} p\right), \\
& \left(\left[[\mathrm{cut}]_{1},[\mathrm{ax}]_{2}\right), p\right),\left(\left(\left[\mathrm{cut}_{1},[\mathrm{ax}]_{3}\right), q!q\right),\left(\left([\mathrm{cut}]_{1},[\mathrm{ax}]_{3}\right), q!p\right),\right. \\
& \left.\left.\left(\left[{ }^{[\mathrm{cut}}\right]_{1},[\mathrm{ax}]_{4}\right), p\right),\left(\left([\mathrm{t}]_{1},[\mathrm{ax}]_{4}\right), 1\right)\right\} .
\end{aligned}
$$

## How to control reduction

In a way similar to that of classical SECD machines we define the state of the machine in terms of four components:

- a stack $S$, which is used to store the current action;
- an environment $E$, is a node of the graph and it provides the local environment where the current action has to be performed;
- a control $C$ is the stream of all actions either provided as initial input or created during the execution of other actions, it has to be executed in the context of the graph stored in the memory of the machine;
- a dump $D$ corresponds to the current graph and represents the global environment for all future actions.
Transitions are therefore given as

$$
(S, E, C, D) \stackrel{\tau}{\mapsto}\left(S^{\prime}, E^{\prime}, C^{\prime}, D^{\prime}\right)
$$

## Full Combustion

0 . If $(S, E, C, D)=(\langle \rangle, \mathrm{NULL}$, nil, $\emptyset)$ then the machine is in its initial state.
The initialisation step is

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read() returns a stream of actions corresponding to the coding of the input in the form of a polarised dynamic graph.

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\left(\rangle, \text { NULL }, C, D) \stackrel{\tau}{\mapsto}\left(C_{v},(\{v\}, \emptyset), C^{\prime}, D\right)\right.
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2. If $S=\alpha:: S^{\prime}, E=(\{\boldsymbol{v}\}, Y)$, and $\alpha=\left\langle\left(\varepsilon, \varepsilon_{s}\right),\left(v, \boldsymbol{v}_{s}\right), w\right\rangle$

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$$
(S, E, C, D) \stackrel{\tau}{\mapsto}\left(S^{\prime},(\{v\}, Y \cup\{\alpha\}), C \ltimes \operatorname{execute}_{v}(\alpha), D\right),
$$

3. When the stack is empty we set the environment to NULL:

$$
(\rangle, E, C, D) \stackrel{\tau}{\mapsto}(\rangle, \text { NULL }, C, D)
$$

## Synchronous Machine

(0) read from input stream
$(\mathbf{0} \otimes \mathbf{0}, \mathrm{NULL} \otimes \mathrm{NULL}$, nil $\otimes$ nil,$\emptyset \otimes \emptyset) \mapsto$

$$
(\mathbf{0} \otimes \mathbf{0}, \mathrm{NULL} \otimes \mathrm{NULL}, \operatorname{read}() \otimes \text { nil }, \emptyset \otimes \emptyset)
$$

(1) actions $\alpha_{1}$ and $\alpha_{2}$ are synchronously extracted from streams $C_{1}$ and $C_{2}$
$\left(\mathbf{0} \otimes \mathbf{0}, \mathrm{NULL} \otimes \mathrm{NULL}, \alpha_{1}:: C_{1}^{\prime} \otimes \alpha_{2}:: C_{2}^{\prime}, D_{1} \otimes D_{2}\right) \mapsto$
$\left(\alpha_{1} \otimes \alpha_{2}, \mathrm{NULL} \otimes \mathrm{NULL}, C_{1}^{\prime} \otimes C_{2}^{\prime}, D_{1} \otimes D_{2}\right)$

## Synchronous Machine (cont.)

(3) simultaneous environment access for both actions:

$$
\begin{aligned}
& \left(\alpha_{1} \otimes \alpha_{2}, \text { NULL } \otimes \mathrm{NULL}, C_{1} \otimes C_{2}, D_{1} \otimes D_{2}\right) \mapsto \\
& \left(\alpha_{1} \otimes \alpha_{2}, v_{t}^{1} \otimes v_{t}^{2}, C_{1} \otimes C_{2}, D_{1}^{\prime} \otimes D_{2}^{\prime}\right)
\end{aligned}
$$

when $\alpha_{i}=\left\langle\epsilon_{i}, \boldsymbol{e}_{i}, w_{i}\right\rangle$ and either $e_{i}=\left(v_{t}^{i}, v_{s}^{i}\right)$ or $v_{t}^{i}$ is undefined if $\alpha_{i}=\mathbf{0}$ then

$$
D_{i}^{\prime}= \begin{cases}D_{i} & \text { if } v_{t}^{i} \text { already is a node of } D^{i}, \\ D_{i} \cup\left\{v_{t}^{i}\right\} & \text { if } v_{t}^{i} \text { is a new node to be added to } D^{i} .\end{cases}
$$

(4) actions execution

$$
\begin{aligned}
& \left(\alpha_{1} \otimes \alpha_{2}, v_{t}^{1} \otimes v_{t}^{2}, C_{1} \otimes C_{2}, D_{1} \otimes D_{2}\right) \mapsto \\
& \left(\mathbf{0} \otimes \mathbf{0}, \text { NULL } \otimes \text { NULL, }\left(\left(C_{1} \otimes \operatorname{execute}_{1}\left(\alpha_{1}\right)\right) \otimes \operatorname{execute}_{1}\left(\alpha_{2}\right)\right) \otimes\right. \\
& \left.\otimes\left(\left(C_{2} \otimes \text { execute }_{2}\left(\alpha_{1}\right)\right) \otimes \operatorname{execute}_{2}\left(\alpha_{2}\right)\right), D_{1}^{\prime} \otimes D_{2}^{\prime}\right)
\end{aligned}
$$

The graph $\left.D_{i}^{\prime}=D_{i} \cup\left(\left(v_{t}^{i}, v_{s}^{i}\right)^{\epsilon_{i}}\right), w_{i}\right)$.

## Aynchronous Machine

The state of the asynchronous machine is annotated with the scheduled processing unit:

$$
(p, S, E, C, D)=\left(p, S_{1} \otimes S_{2}, E_{1} \otimes E_{2}, C_{1} \otimes C_{2}, D_{1} \otimes D_{2}\right)
$$

where $p \in\{1,2\}$ is the order number of the scheduled processor.

The sequence of controls $p$ is by itself a stream (of integers $\{1,2\}$ ). We may either choose a random sequence or we may force a particular scheduling by explicitly giving it.

## Asynchronous parallel SECD

0 reading from the input interface:
$(1, \mathbf{0} \otimes \mathbf{0}$, NULL $\otimes$ NULL, nil $\otimes$ nil,$\emptyset \otimes \emptyset) \mapsto$
$\quad(1, \mathbf{0} \otimes \mathbf{0}$, NULL $\otimes$ NULL, $\operatorname{read}() \otimes$ nil,$\emptyset \otimes \emptyset)$
(1) action $\alpha_{p}$ extraction from the stream $C_{p}$ :

$$
\begin{aligned}
& \left(p, S_{1} \otimes S_{2}, E_{1} \otimes E_{2}, C_{1} \otimes C_{2}, D_{1} \otimes D_{2}\right) \mapsto \\
& \quad\left(p^{\prime}, S_{1}^{\prime} \otimes S_{2}^{\prime}, E_{1}^{\prime} \otimes E_{2}^{\prime}, C_{1}^{\prime} \otimes C_{2}^{\prime}, D_{1}^{\prime} \otimes D_{2}^{\prime}\right)
\end{aligned}
$$

if $S_{p}=\mathbf{0}, E_{p}=\mathrm{NULL}, C_{p}=\alpha_{p}:: C_{p}^{\prime}$ then

$$
S_{i}^{\prime}= \begin{cases}S_{i} & \text { if } i \neq p \\ \alpha_{i} & \text { if } i=p\end{cases}
$$

$E_{i}^{\prime}=E_{i}, C_{i}^{\prime}=C_{i}$ if $i \neq p$ and $D_{i}^{\prime}=D_{i}$, finally $p^{\prime}$ is taken in accord to the scheduling function.

## Asynchronous parallel SECD (cont.)

(2) action $\alpha_{p}$ 's environment access:

$$
\begin{aligned}
& \left(p, S_{1} \otimes S_{2}, E_{1} \otimes E_{2}, C_{1} \otimes C_{2}, D_{1} \otimes D_{2}\right) \mapsto \\
& \quad\left(p^{\prime}, S_{1}^{\prime} \otimes S_{2}^{\prime}, E_{1}^{\prime} \otimes E_{2}^{\prime}, C_{1}^{\prime} \otimes C_{2}^{\prime}, D_{1}^{\prime} \otimes D_{2}^{\prime}\right)
\end{aligned}
$$

when $S_{p}=\alpha_{p}=\left\langle\epsilon_{p}, \boldsymbol{e}_{p}, w_{p}\right\rangle$, where

$$
E_{i}^{\prime}= \begin{cases}E_{i} & \text { if } i \neq p \\ v_{t}^{p} & \text { if } i=p\end{cases}
$$

$S_{i}^{\prime}=S_{i}, C_{i}^{\prime}=C_{i}$ and

$$
D_{i}^{\prime}= \begin{cases}D_{i} & \text { if } i \neq p \text { or } i=p \text { and } v_{t}^{p} \in D_{p}, \\ D_{i} \cup\left\{v_{t}^{p}\right\} & \text { if } i=p \text { and } v_{t}^{p} \notin D_{p} .\end{cases}
$$

## Asynchronous parallel SECD (cont.)

(3) action execution:

$$
\begin{aligned}
& \left(p, S_{1} \otimes S_{2}, E_{1} \otimes E_{2}, C_{1} \otimes C_{2}, D_{1} \otimes D_{2}\right) \mapsto \\
& \left(p^{\prime}, S_{1}^{\prime} \otimes S_{2}^{\prime}, E_{1}^{\prime} \otimes E_{2}^{\prime}, C_{1}^{\prime} \otimes C_{2}^{\prime}, D_{1}^{\prime} \otimes D_{2}^{\prime}\right)
\end{aligned}
$$

when $S_{p}=\alpha_{p}=\left\langle\epsilon_{p}, \epsilon_{p}, w_{p}\right\rangle, E_{p}=v_{t}^{p}$, then

$$
\begin{gathered}
S_{i}^{\prime}=\left\{\begin{array}{ll}
S_{i} & \text { if } i \neq p \\
0 & \text { if } i=p
\end{array} \quad E_{i}^{\prime}= \begin{cases}E_{i} & \text { if } i \neq p \\
\text { NULL } & \text { if } i=p\end{cases} \right. \\
C_{i}^{\prime}=C_{i} \otimes \text { execute }_{i}\left(\alpha_{p}\right)
\end{gathered}
$$

and the graph $D_{i}^{\prime}=D_{i}$ for all $i \neq p$ and $D_{p}^{\prime}$ is obtained from $D_{p}$ by adding the edge $\left(\left(v_{t}^{p}, v_{s}^{p}\right)^{\epsilon_{p}}, w_{p}\right)$.

## New ideas to extend the computation

The parallel machine can read from two different channels and execute "in parallel" reading the interpretation of two terms:
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The parallel machine can read from two different channels and execute "in parallel" reading the interpretation of two terms:

$$
(\rangle, \text { NULL, nil }, \emptyset) \stackrel{\tau}{\mapsto}(\rangle, \text { NULL, } \operatorname{read}(0) \oplus \operatorname{read}(1), \emptyset) .
$$

the only missing part is now the way we can read-back the two results which are independently evaluated by the machine.

Execution in the asynchronous version can now be extended to execute non-deterministic within a processor one of the two terms, so schedules are of type $p . b$ wher $p$ is the processor and $b$ is one of the two subterms.

